



भारतीय प्रौद्योगिकी संस्थान इन्दौर  
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IIT Indore

## **Walk-in Interview for SRF/RA Position**

Applications are invited from motivated and eligible candidates for the position of **Research Associate/Senior Research Fellow** (RA/SRF) in a Council of Scientific & Industrial Research (CSIR) funded research project entitled “**Design and development of integrated nano photonic platform based on hybrid photonic-plasmonic waveguide for communication and bio-sensing**”. Selected RA/SRF will be involved in design, fabrication (using electronic micro-fabrication technologies) and optical characterization of on-chip photonic devices.

**Duration:** Initial appointment is for one year, which is extendable further based on performance. *Selected candidate may be considered for admission to Ph.D. Programme as a regular full-time scholar.*

**Emolument:** The amount of RA/SRF fellowship will be as per CSIR EMR-II norms.

### **Eligibility:**

**Essential Qualification:** For (RA) – PhD in Electronics or related areas **OR** ME/MTech in relevant branch of Electronics/ Electronics and Communication Engineering/ Microelectronics/ Optoelectronics/ Nanotechnology/ VLSI Design with three years of experience in research/teaching/design and development. For (SRF) M.E./M.Tech. in relevant branch of Electronics/ Electronics and Communication Engineering/ Microelectronics/ Optoelectronics/ Nanotechnology/ VLSI Design. Candidates should have a minimum of 60 % (or equivalent) in graduate and postgraduate levels.

**Age limit:** 32/35 years (SRF/RA). Relaxation for SC/ST/OBC candidate as per Government of India norms.

Interested candidates are required to attend walk in interview on **6<sup>th</sup> October, 2016**. The candidates should bring Detailed Curriculum Vitae along with valid photographic identity card, original and self-attested photocopies of all documents (10th class onwards) at the time of interview. Incomplete applications will be rejected.

**Time and date of walk-in interview:** 09:00 hrs on **Thursday, 06 October, 2016**.

**Venue:** Control System Laboratory, Department of Electrical Engineering, First Floor, "POD CSE-2", Indore Indian Institute of Technology (IIT) Indore, Khandwa Road, Simrol, Indore - 452020, Madhya Pradesh.

Interested candidates must email advance copy of their detailed CV to Dr. Mukesh Kumar at [mukesh.kr@iiti.ac.in](mailto:mukesh.kr@iiti.ac.in).

No TA/DA will be paid for attending the walk-in interview. Candidates should make their own arrangements for stay.